



भारत सरकार /Government of India  
अंतरिक्ष विभाग/Department of Space  
भारतीय अंतरिक्ष विज्ञान एवं प्रौद्योगिकी संस्थान

INDIAN INSTITUTE OF SPACE SCIENCE AND TECHNOLOGY

वलियमला पोस्ट / Valiamala PO, तिरुवनंतपुरम / Thiruvananthapuram-695 547

Indian Institute of Space Sciences and Technology (IIST) invites application for the following project positions to work in the research projects:

Category No.	Project Title	Educational Qualification/Skill	Consolidated monthly remuneration	No. of positions
<b>PROJECT ASSOCIATE I /PROJECT ASSOCIATE II</b>				
PA-I-03/2025	Development of sustainable and bio-derived high surface area silica gel for Industrial Applications	<p><b>Desired Qualification:</b> M.Sc. Chemistry /Material Science and related areas</p> <p><b>Desired Experience</b> Experience in synthesis and characterization of inorganic materials (minimum of 3 months)</p> <p><b>Preferrable Experience /Exposure</b> Lab experience in synthesis and characterization of inorganic/porous nanomaterials (Tenure- 6 months)</p>	Project Associate-I ₹31,000/- plus HRA	01
PA-I-01/2026 Or PA-II-01/2026	Powertrain for All-Electric HANSA NG-Technology Demonstration	<p><b>Project Associate 1:</b> <b>Desired Qualification:</b> B. E or B.Tech in Electrical Engineering, Instrumentation and related areas.</p> <p><b>Project Associate 2:</b> <b>Desired Qualification:</b> M.E / M.Tech in Power Electronics or related areas. Or B. E /B.Tech in Electrical Engineering, Instrumentation and related areas and with at least 2 years of experience in scientific labs, research labs or industry.</p> <p><b>Desired Experience</b> Exposure to simulation and building power converters, embedded systems and programming, testing of power converters.</p> <p><b>Preferrable Experience /Exposure</b></p>	Project Associate I ₹31,000/- plus HRA  Or Project Associate II- ₹35,000/- plus HRA	02

<p>Design of electronic systems, PCB layout and PCB assembly.</p>	<p><b>Key Responsibilities</b></p> <p><b>Electronics Design:</b> Design and capture schematics (e.g., power, communication, data handling). Select electronic components, considering their performance, reliability, and radiation tolerance. Perform PCB layout design and collaborate with manufacturers to ensure manufacturability and quality.</p> <p><b>System Development:</b> Develop, test, and debug embedded firmware for microcontrollers and FPGAs. Integrate and test electronic hardware with other subsystems. Create and maintain detailed documentation, including design specifications, test procedures, and reports.</p> <p><b>Verification and Testing:</b> Develop and execute comprehensive test plans for functional, performance, and environmental testing (e.g., thermal, vacuum, vibration). Conduct fault analysis and troubleshooting of electronics at the component and system level. Support Assembly, Integration, and Test (AIT) activities to ensure mission readiness.</p> <p><b>Required Skills</b></p> <p><b>Design Proficiency:</b> Strong knowledge of schematic capture and PCB layout software (e.g., Altium Designer, OrCAD, Cadence Allegro). Experience with embedded systems, including microcontrollers (e.g., ARM Cortex) and digital logic (FPGA/CPLD).</p> <p><b>Testing &amp; Analysis:</b> Proficiency with lab equipment such as oscilloscopes, multimeters, spectrum analyzers, and power supplies. Experience in conducting hardware-level testing, including power-on</p>	
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		<p>self-tests and functional verification.</p> <p><b>General Skills:</b></p> <p>Excellent problem-solving skills and attention to detail.</p> <p>Strong written and verbal communication abilities.</p> <p>Willingness to adapt and learn new technologies from open-source resources.</p> <p><b>Note:</b> The offered position of Project Associate-1 or Project Associate-2 will depend on the candidate's performance in the written test/interview, whichever is applicable.</p>		
PA-I-04/2025  Or  PA-II-06/2025	SSPACE	<p><b>Desired Qualifications:</b> B.E/B.Tech in Aerospace Engineering / Aerospace Engineering with specialization in Avionics / Aeronautical Engineering / Mechanical Engineering.</p> <p><b>Key Responsibilities:</b> Design and develop structural components for satellite systems. Perform detailed static and dynamic analysis to ensure structural integrity under launch and operational conditions. Contribute to the full lifecycle of structural design, from initial concept to final testing. Collaborate with cross-functional teams to integrate structural designs with other satellite subsystems.</p> <p><b>Required Skills:</b></p> <ul style="list-style-type: none"> <li>• <b>Proficiency in CAD Software:</b> Demonstrated knowledge of SolidWorks for 3D modeling and part design.</li> <li>• <b>Structural Analysis Expertise:</b> Strong understanding and practical experience in performing both static and dynamic analysis (e.g., stress, strain, modal analysis) of mechanical components.</li> <li>• <b>Problem-Solving:</b> Ability to solve complex engineering challenges related to space-grade structures.</li> </ul> <p><b>Candidate Profile</b></p>	<p>Project Associate-I ₹31,000/- plus HRA</p> <p>Or</p> <p>Project Associate-II ₹35,000/- plus HRA</p>	01

		<p><b>Eagerness to Learn:</b> A self-starter with a strong willingness to acquire new knowledge and skills independently from open-source resources.</p> <p><b>Adaptability:</b> The ability to apply theoretical knowledge to practical, hands-on design and analysis tasks.</p> <p><b>Collaboration:</b> Excellent communication skills for working effectively within a team environment.</p> <p><b>Expected Job Roles and Responsibilities:</b></p> <ul style="list-style-type: none"> <li>• <b>Design &amp; Manufacturing:</b> Create designs and production files for structures and mechanisms for space and aircraft systems.</li> <li>• <b>Procurement &amp; Sourcing:</b> Identify and recommend components and vendors in collaboration with sub-system teams.</li> <li>• <b>Prototyping &amp; Testing:</b> Develop experimental and validation test setups as needed for prototyping.</li> </ul> <p><b>Documentation &amp; Reporting:</b> Compile and record all documentation related to indentation and tender procedures for presentation to the review committee.</p> <p><b>Note:</b> The offered position of PA-I/PA-II will depend on the performance and relevant experience of the candidate in the written test/interview whichever is/are applicable.</p>		
PA-I-05/2025  Or  PA-II-07/2025	SSPACE	<p><b>Qualifications:</b> B.E/B.Tech in ECE/Avionics/Electronics or related areas</p> <p><b>Key Responsibilities</b></p> <ul style="list-style-type: none"> <li>• <b>Electronics Design:</b></li> <li>• Design and capture schematics for various satellite subsystems (e.g., power, communication, data handling).</li> <li>• Select space-grade electronic components, considering their performance, reliability, and radiation tolerance.</li> <li>• Perform PCB layout design and collaborate with manufacturers to ensure manufacturability and quality.</li> </ul>	Project Associate-I ₹31,000/- plus HRA  Or  Project Associate-II ₹35,000/- plus HRA	01

- **System Development:**
  - Develop, test, and debug embedded firmware for microcontrollers and FPGAs.
  - Integrate and test electronic hardware with other satellite subsystems.
  - Create and maintain detailed documentation, including design specifications, test procedures, and reports.
- **Verification and Testing:**
  - Develop and execute comprehensive test plans for functional, performance, and environmental testing (e.g., thermal, vacuum, vibration).
  - Conduct fault analysis and troubleshooting of electronics at the component and system level.
  - Support Assembly, Integration, and Test (AIT) activities to ensure mission readiness.

#### **Required Skills**

- **Design Proficiency:**
  - Strong knowledge of schematic capture and PCB layout software (e.g., Altium Designer, Or CAD, Cadence Allegro).
  - Experience with embedded systems, including microcontrollers (e.g., ARM Cortex) and digital logic (FPGA/CPLD).
- **Testing & Analysis:**
  - Proficiency with lab equipment such as oscilloscopes, multimeters, spectrum analyzers, and power supplies.
  - Experience in conducting hardware-level testing, including power-on self-tests and functional verification.
- **General Skills:**
  - Excellent problem-solving skills and attention to detail.
  - Strong written and verbal communication abilities.

		<ul style="list-style-type: none"> <li>• Willingness to adapt and learn new technologies from open-source resources.</li> </ul> <p><b>Note:</b> The offered position of PA-I/PA-II will depend on the performance and relevant experience of the candidate in the written test/interview whichever is/are applicable.</p>		
PA-I-06/2025	Design and Construction of MEMS-based Portable Seismocardiogram for on-board Cardiac health monitoring of Astronauts	<p><b>Desired Qualification:</b> MSc/M.Tech in Physics /Material Science/ Nanoscience/ Nanotechnology/ Artificial Intelligence.</p> <p><b>Desired Experience</b> Programming skills, Familiarity with AI</p> <p><b>Preferrable Experience /Exposure</b> Familiarity with AI tools. (Tenure- 5 months)</p>	Project Associate-I ₹31,000/-	01
PA-I-07/2025	RIS aided joint localization and Communication TTDF/6G/86	<p><b>Desired Qualification:</b> B.E/B. Tech in ECE/Avionics/Electronics or related areas with GATE qualification</p> <p><b>Desired Experience</b> Knowledge in RF Simulation Software CST, ADS, HFSS</p> <p><b>Preferrable Experience /Exposure</b> Knowledge in RF Circuit design, antenna design</p>	Project Associate-I ₹31,000/-	01

#### SENIOR PROJECT ASSOCIATE

SPA-03/2025	Development and Prototyping Wideband Spectrum sharing and Sensing Systems for efficient Spectrum utilization	<p><b>Desired Qualification:</b> M.Tech in RF and Microwave/Digital Signal Processing with experience in R &amp; D in industry/Academic Institutes.</p> <p><b>Desired Experience</b> Experience in design, simulation measurement of antenna, and strong background in programming languages like MATLAB/ PYTHON.</p> <p><b>Preferrable Experience /Exposure</b> Research experience in design of antennas, digital signal processing including algorithm development, implementation &amp; analysis.</p>	Senior Project Associate ₹42,000/- plus HRA	01
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#### JUNIOR PROJECT FELLOW

JPF-07/2025	Development of Dynamic Spoofing technology for Drone/UAV	<p><b>Desired Qualification:</b> B Tech in ECE/Computer Science with GATE qualification</p> <p><b>Desired Experience</b></p>	Junior Project Fellow ₹25,000/-	
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		<ol style="list-style-type: none"> <li>1. Strong knowledge of Python Programming and C Programming</li> <li>2. Good understanding of Signal Processing Concepts and Applications.</li> </ol> <p><b>Preferrable Experience /Exposure</b> Experience or familiarity with GPS Signal Processing, SDR/USRF</p>		
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**Maximum Age limit 35 years as on 09.02.2026**

Category code	Monthly Remuneration	No. of positions
Project Associate-I or Project Associate-II	₹31,000/- plus HRA Or ₹35,000/- plus HRA	04
Project Associate-I	₹31,000/- plus HRA	03
Senior Project Associate	₹42,000/- plus HRA	01
Junior Project Fellow	₹25,000/-	01

**General Conditions/Instructions:**

1. Only Indian nationals need apply.
2. All degrees mentioned above should be in **FIRST CLASS**. Candidates should possess the required qualification as on the last date for receipt of application.
3. Candidates are advised to upload the relevant certificate such as mark sheet, Degree/PG Certificate, Experience certificate etc. at the time of registering the online application.
4. The post is initially for a period of one year. The tenure is extendable on performance and need basis subject to availability of funds.
5. The Institute reserves the right to terminate the appointment at any time before completion of the tenure/project, if it so decides.
6. Application received online only will be considered for processing under any circumstances.
7. The applicants will not be allowed to make any changes in the profile registration once submitted. Hence utmost care should be taken by the candidates while filling the profile.
8. Facility for online submission of application will be available from **09<sup>th</sup> February, 2026 to 18<sup>th</sup> February, 2026**
9. 'No Objection Certificate' from the employer concerned is required in respect of those applicants who are employed under Central/State Government/Public Sector Undertakings/Autonomous Bodies.
10. Only short listed candidates will be called for written test/interview as the case may be. Candidates possessing advertised qualification, skill, experience etc., alone will be allowed to attend the written test/interview.

11. Government strives to have a work force which reflects gender balance and women candidates are encouraged to apply.
12. Candidates who are selected for the positions have to join IIST by the specified date.
13. The above positions are not entitled for hostel facility at IIST.
14. Candidates are advised to visit the IIST website <https://www.iist.ac.in/project-positions> for any updates on latest status of their application.
15. No interim correspondence will be entertained.
16. Canvassing in any form will be a disqualification.
17. **Candidates shortlisted will be called for online interview.**

**Advt No,IIST/Admin/16-02/2026 dt. 09.02.2026**

**Email: projectrecruitment@iist.ac.in/Phone No.0471-2568601/643**