



Government of India, Department of Space  
 Indian Institute of Space Science and Technology  
 [Declared as Deemed to be University under Sec.3 of the UGC Act 1956]  
 Valiamala PO, Thiruvananthapuram -695 547, Kerala

**ADDENDUM to the Advt. No. IIST/Admn/RMT/10(b)/2019 dated 19.10.2019**

Following position is also included in the advertisement for Project Staff advertised vide Advt. No. IIST/Admn/RMT/10(b)/2019 dated 19.10.2019:

<b>Junior Research Fellow (JRF)</b>					
Sl. No	Category No.	Qualification / Skill		Project Title	No. of positions
1	JRF-13/2019	ME or M.Tech or equivalent post-graduate degree in 1. Electronics and Communication Engineering 2. Applied Electronics and Instrumentation 3. Instrumentation and Control 4. Electrical Engineering or 5. Power Electronics 6. Embedded Systems 7. Microelectronics OR Related areas <u>Desired experience/ exposure:-</u> <ul style="list-style-type: none"> <li>• Analog Electronic design</li> <li>• Digital circuits design</li> <li>• Embedded systems</li> <li>• Precision circuit design with opamps</li> <li>• Simulations with SPICE</li> <li>• Circuit schematics and layout using CAD tools.</li> </ul>	<b>Note:</b> The selected candidate can register for IIST PhD programme subject to fulfilment of eligibility conditions for PhD admission in IIST. Candidates must have done M Tech through a valid GATE score in Electronics and Communication/ Electrical/ Instrumentation. For further information in this regard please refer to <a href="https://www.iist.ac.in/admissions/phd/regular">https://www.iist.ac.in/admissions/phd/regular</a>	Precision Electronics Systems Engineering	01
		<u>Syllabus for written test</u> Topics related to Electrical Circuits and Network theorems, Signals and Systems, Analog Electronics, Digital Electronics and Basics of sensors/ instrumentation present in GATE Electronics and Communication/ Instrumentation syllabus.			

**Remuneration per month (Consolidated):** Rs. 31,000/- + HRA

**Age limit & General Conditions/ Instructions** are as mentioned in the Advt. No. IIST/Admn/RMT/10(b)/2019 dated 19.10.2019.