

Government of India, Department of Space Indian Institute of Space Science and Technology [Declared as Deemed to be University under Sec.3 of the UGC Act 1956] Valiamala PO, Thiruvananthapuram -695 547, Kerala

<u>Application for Research / Project Fellow (Online Application only)</u>

Indian Institute of Space Science and Technology (IIST) invites application for the following Research / Project Fellow positions to work in the different Research Projects:

SI. No.	Category No.	Qualification / S	Skill	Project Title	No. of positions		
	Junior Research Fellow (JRF)						
1	JRF- 03/2019	M.Sc in Physics / Astrophysics / Astronomy / Space Science or (ii) B.S / M.S in Physics or equivalent or (iii) M.S in Astronomy & Astrophysics or (iv) B.E/B.Tech / M.E / M.Tech degree (any branch) with UGC-CSIR National Eligibility Test in Physical Sciences or GATE / JEST score in Physics.	Note: The selected candidate can register for IIST PhD programme subject to fulfilment of eligibility conditions for PhD admission in IIST. For further information in this regard please refer to https://www.iist.ac.in/admissions/phd/regular	Understanding the Physical Conditions of Baryons Outside of Galaxies in the Low Redshift Universe. (Externally Funded Project)	01		
2	JRF- 04/2019	M.S / M.Sc in Physics / Electronics / Space Physics / Space Sciences or related areas. Desirable:- Knowledge of vacuum technology, charged particle detector electronics & pulse processing. A written test will be conducted for candidates without GATE / JEST / UGC-CSIR National Eligibility Test		Electron impact secondary electronion coincidence spectrometer for investigation of dissociation dynamics of PAHs. (Externally Funded Project)	01		
3	JRF- 05/2019	M.E / M.Tech / M.S by research or equivalent in Electronics/ Communication / Electrical and Electronics/ Electronics and Communication / Communication Networks / Telecommunication / Signal Processing / Machine Learning / Computer Science/ Control Systems / System Science / Data Analysis or related areas.		Wireless ReLoD – Wireless Reliable, Low Latency Networks for IIoT and Field bus replacement. (Externally Funded Project)	01		

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		Master's Degree Engineering / Technology should have been pursued on the basis of qualified GATE score.			
	Desirable:- 1. One year of industry experience. 2. Good demonstrable programming skills in C/C++/Python/Matlab and / or good demonstrable mathematical and analytical skills				
		Syllabus for Written Test "Communication" as in the EC GATE 2019 syllabus			
	Senior Project Fellow (SPF)				
4	SPF- 04/2019	ME / M.Tech in Power Electronics / Industrial Electronics / Control Systems / Electrical Engineering or related areas with Power Electronics as a core course. Desirable:- Experience in building power electronics based systems	Development of Control Schemes for Multi-phase Dual Converter FED Open-end Winding BLDC / Induction Motor Drives	01	
Junior Project Fellow (JPF)					
5	JPF- 04/2019	BE / B.Tech in Electronics or related areas. Desirable:- 1. Good knowledge in Semiconductor Physics. 2. Experience in 2D materials.	Possibility of Finding Graphene as THZ Detector Materials	01	

Age limit: 35 years as on May 06, 2019.

Remuneration per month (Consolidated):

Catogory Code	Monthly Remuneration	
JRF-03/2019 to JRF-05/2019	Rs. 25,000/- (Under Revision) + HRA	
SPF-04/2019	Rs. 22,000/-	
JPF-04/2019	Rs. 20,000/-	

General Conditions/ Instructions:

- 1. Only Indian Nationals need apply.
- 2. All degrees mentioned above should be in FIRST CLASS. Candidates should posses the required qualification as on the last date for receipt of application.
- 3. Candidates are advised to upload the relevant mark sheets and the Degree/ PG certificate at the time of registering the online application.
- 4. The tenure of the position is for a period of One year or co-terminus with the project whichever is earlier. The tenure is extendable on performance and need basis subject to availability of funds.
- 5. The Institute reserves the right to terminate the appointment at any time before completion of the tenure / project, if it so decides.

- 6. Application received online only will be considered for processing under any circumstances.
- 7. The applicants will not be allowed to make any changes in the profile registration once submitted. Hence utmost care should be taken by the candidates while filling the profile.
- 8. Facility for online submission of application will be available from April 18, 2019 (1600hrs onwards) to May 06, 2019 (upto1700hrs).
- 9. A 'No Objection Certificate' from the employer concerned is required in respect of those applicants who are employed under Central/State Government/Public Sector Undertakings/ Autonomous Bodies.
- 10. Only short listed candidates will be called for written test/interview, as the case may be, based on merit.
- 11. Outstation candidates called for interview will be paid 'to and fro' Second class Railway Fare by the shortest route including reservation / sleeper charges on production of proof of journey such as Railway Tickets / Bus Tickets, etc. If the candidates travel in higher class of accommodation than the one prescribed, only Second class rail fare excluding the reservation charges / sleeper charges will be paid. The applicants are advised to make their own arrangements for their stay at Thiruvananthapuram, at their cost.
- 12. Government strives to have a work force which reflects gender balance and women candidates are encouraged to apply.
- 13. Candidates who are selected for the positions have to join IIST by the specified date.
- 14. Research / Project Fellows are not entitled for hostel facility at IIST.
- 15. Candidates are advised to visit the IIST website https://www.iist.ac.in/career/3 for any updates on latest status of their application.
- 16. No interim correspondence will be entertained.
- 17. Canvassing in any form will be a disqualification

IMPORTANT DATES						
SI. No.	Event	Date				
1	Opening of IIST website for online submission of applications	April 18, 2019 - 1600 hrs (Thursday)				
2	Closing of IIST website for online submission of applications	May 06, 2019 - 1700 hrs (Monday)				
3	Display of shortlisted candidates for Test/Interview	May 16, 2019-1700hrs (Thursday)				
4	Date of written screening test (For candidates registering for PhD programme)	May 26, 2019 (Sunday) at 0930 hrs				
5	Publishing of written screening test results (For candidates registering for PhD programme)	May 31, 2019 – 1700 hrs (Friday)				
6	Interview Dates (For candidates registering for PhD programme)	June 10 & 11 (Monday & Tuesday)				
7	Display of Provisionally selected candidates (For candidates registering for PhD programme)	June 14, 2019 - 1700 hrs (Friday)				
8	Last Date of remittance of semester fee (For candidates registering for PhD programme)	June 21, 2018 (Friday)				
9	Classes begin for Ph.D Programme	July 22, 2019 (Monday)				